

IN THE CLAIMS

1. – 6. (canceled)

7. (previously presented) The transmission apparatus as claimed in claim 11, wherein the synchronous pulse generating part generates clock change timing by using a timer.

8. (previously presented) The transmission apparatus as claimed in claim 11, wherein the synchronous pulse generating part generates clock change timing by using lock detection for a phase locked loop.

9. (previously presented) The transmission apparatus as claimed in claim 11, wherein the synchronous pulse generating part comprises:

a window generating part configured to generate a narrow window and a wide window for monitoring phase of clock change timing; and

a timing generating part configured to generate clock change timing by monitoring said phase of clock change timing with the narrow window during a monitoring period and by switching the narrow window to the wide window if a predetermined condition is satisfied.

10. (currently amended) A transmission apparatus for cross-connecting channels on a synchronous multiplex transmission network, the transmission apparatus comprising interface parts and a common part that includes a cross-connecting part and memories each corresponding to one of the interface parts,

each of the interface parts comprising:

a synchronous pulse generating part configured to generate a reference timing pulse that is synchronized among the interface parts; and

a frame generating part configured to generate a frame for synchronous multiplex transmission based on the reference timing pulse and outputs the frame to the common part,

wherein each frame output from the interface parts is written into one of the memories in the common part, and the cross-connecting part reads out each frame from the memories so that the common part adjusts phases of frames output from the interface parts using the memories.

11. (previously presented) The transmission apparatus as claimed in claim 10, wherein the synchronous pulse generating part receives a timing pulse from the common part to perform phase adjusting of the timing pulse by performing clock change for the timing pulse.